

**IN THE ABSTRACT**

Please amend the Abstract, to read as follows:

A method for controlling the power consumption in a tilt correcting coil is disclosed. The power consumption is corrected in the tilt correcting coil for correcting the tilt of the images of the cathode ray tube. If a microcomputer judges that the mode is the on-state mode, then the microcomputer outputs a tilt correcting PWM signal in accordance with the user's inputting. Then the output tilt correcting PWM signal is converted into a dc voltage, and the level is adjusted. Then the signal is supplied to the tilt correcting coil, so that the tilt of the image on the screen would be corrected. In the cases of the standby mode, the suspend mode and/or the power-off mode, the microcomputer outputs a signal which has a function of minimizing the power consumption of the tilt correcting coil. Therefore, the tilt of the image of the screen is corrected in the normal manner in the on-state mode. On the other hand, in the cases of the standby mode, the suspend mode and/or the power-off mode, the tilt correcting coil does not consume any power, thereby satisfying the power consumption definition of the power-off mode.

**IN THE CLAIMS**

Please amend claims 8, 9, 11 and 12, and add claims 13 through 57, to read as follows:

1           8. (Amended) The apparatus as set forth in claim 6, said microcomputer outputting a signal  
2           having a constant high logic level, when either one of said horizontal and vertical synchronizing